



Advanced UVM

Brian Hunter

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Since its introduction in 2011, the Universal Verification Methodology (UVM) has achieved its promise of becoming the dominant platform for semiconductor design verification. Advanced UVM delivers proven coding guidelines, convenient recipes for common tasks, and cutting-edge techniques to provide a framework within UVM. Once adopted by an organization, these strategies will create immediate benefits, and help verification teams develop scalable, high-performance environments and maximize their productivity. The second edition updates the chained sequencer, re-organizes the content, and has a few minor corrections. “Written by an experienced UVM practitioner, this book contains lots of great tips on using UVM effectively and example code that actually works!” ~John Aynsley, Doulos “In ‘Advanced UVM’, Mr. Hunter, based on his company’s real world experiences, provides excellent resources, a well-tested reference verification environment, and advanced best practices on how to apply UVM. If you are ready to move beyond a UVM introduction, this should be the book you add to your library.” ~George Taglieri, Director Verification Product Solutions, Synopsys, Inc.

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