Microprocessor & Assembly Language Programming

Mrs. Deepali A Godse, Mr. Atul P Godse

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Microprocessor & Assembly Language Programming Mrs. Deepali A Godse, Mr. Atul P Godse Microprocessor Architecture : Basic computer architecture using 8085 MP I/O, Memory &system buses, Instruction execution sequence &data flow, Instruction cycle concept of address, Bus, Data bus data &Control bus. Synchronous &Asynchronous buses. I/O Memory Interface : Programmable, Interrupted initiated, DMA transfer serial and parallel interface, Detail study of 8251 I/O processor. Instruction Set &Assembly Languages Programming : Introduction, Instruction &data formats, Addressing modes, Status flags, All 8085 instructions, Data transfer groups, Arithmetic group, Logical group, Branch group. Microprocessor Development System (MDS) : PROM programming, Emilator, ROM simulation introduction to up kits, Study of 8051, C single chip microcomputer. Case Structure &Microprocessor Application : Up application interfacing a matrix keyboard 7-segment led display. Study of traffic light system stepper motor interface. Basic Architect : Introduction to 8086, Motorola 68000.

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